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| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99) | Application Number | | 10750714 | |
| | Filing Date | | 2003-12-31 | |
| | First Named Inventor | Ricardo E. Gonzalez | | |
| | Art Unit | 2181 | | |
| | Examiner Name | Benjamin P. Geib | | |
| | Attorney Docket Number | PA2683US | | |

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| | 1 | 5293489 | | 1994-03-08 | Furui et al. | |

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| 1 | Hauck et al., "The Chimaera Reconfigurable Functional Unit", Proceedings of the 5th Annual Symposium on FPGAs for Custom Computing Machines, April 16-18, 1997, IEEE, pp. 87-96. | <input type="checkbox"/> |
| 2 | Lu et al., "The MorphoSys Dynamically Reconfigurable System-On-Chip", Proceedings of the First NASA/DoD Workshop on Evolvable Hardware, IEEE, July 19-22, 1999, pp. 152-160. | <input type="checkbox"/> |
| 3 | XILINX, "Virtex™ 2.5 V Field Programmable Gate Arrays, Product Specification," DS003-2 (v2.8.1) December 9, 2002, pp. 1-20. | <input type="checkbox"/> |
| 4 | Michael J. Wirthlin et al., "DISC: The dynamic instruction set computer," Proceedings SPIE, Field Programmable Gate Arrays (FPGAs) for Fast Board Development and Reconfigurable Computing, October 25-26, 1995, 14 pages. | <input type="checkbox"/> |

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